

ABSTRACT

A memory system has a circuit board provided with a first slot
5 connector into which a first memory module is inserted. A second slot connector is provided into which a second memory module is inserted. The first and the second memory modules are connected via a flexible bridge. The flexible bridge extends from respective ends of the memory modules opposite to that
10 ends thereof which are inserted into the connector slots. The flexible bridge provides a signal bus between the memory modules.

Fig. 1